

compiler is a softw. prog. that converts the high level lang. or source code into low level lang. or machine code that can be understood by the computer or machine. The proc. of converting the source code into machine code involves phases:

1. Lexical analysis: The 1st phase of compiler. This phase reads the code & breaks it into a stream of tokens. The tokens are then passed to next phase for further processing.
2. Syntax analysis: The 2nd phase of compiler. This phase takes the stream of tokens generated by lex. ana. & checks whether the tokens are grammar of

prog. lang. The op of this phase is a ~~tree~~ syntax tree. 3. Semantic analysis: The 3rd phase checks whether the code is semantically correct. 4. Interm. code generation: This phase generates an interm. representation of the source code that can be easily translated into machine code. 5. Code optimization: This phase applies various optimization techs. to interm. code to improve performance of the generated machine code. 6. Code generation: This phase takes optimized interm. code & generates actual machine code that can be executed by machine.

Specification of Token: There are 3 specifications of token: 1. Strings: are a finite set of symbols or char.s. These symbols can be a digit or an alphabet. 2. Lang. can be defined as a finite set of strings over some symbols or alphabets. 3. Regular Expression are str.s of char.s that define a searching pattern with which we can form a lang., & each RE represents a lang. A RE 'r' can denote a lang L(r) which can be built recursively over the smaller RE by following rules $\rightarrow$ Kleene closure (*), positive closure (+)

Recognition of Tokens: Tokens are recognized using lexemes. $\rightarrow$ digit $\rightarrow$ [0-9]
 number $\rightarrow$ digit (E (+|-)? digit) (letter)
 [A-Z a-z] Id $\rightarrow$ letter (letter | digit)*
 if $\rightarrow$ if, then $\rightarrow$ then, else $\rightarrow$ else, relop
 $\rightarrow$ < | > | <= | >= | =

Syntax analysis
 unknown as parsing is a proc. where the compiler checks if the source code follows the grammatical rules of prog. lang. The main goal of syn. ana. is to create a parse tree of the source code, which

In a Lang. proc. sys. the source code is 1st preprocessed. The modified source code or pre-processed code is then processed by the compiler to form the target assembly lang. which is then translated by the assembler to create relocatable obj. code that are processed by linker & loader to create the target prog. or machine code. Bootstrapping is a proc. in which simple lang. is used to translate more complicated prog. which in turn may handle for more complicated prog. This complicated prog. can further

handle more complicate prog. & so on. $\rightarrow$ It is also called self-compiling compiler written in its source lang. Porting is the proc. of modifying the existing compiler for the target env. to work on a diff. machine. I/O buffering is a tech. involves reading a block of I/O chars from source code & grouping them into meaningful units called tokens. I/O buffering improves performance & reduce overhead.

is a hierarchical representation of the source code that reflects the grammatical structure of the prog. There are some types of parsing alg.s used in syn. ana. $\rightarrow$ LL parsing: This is a top down parsing alg. that starts with the root of parse tree & constructs the tree by successively expanding non-terminals. LL parsing is simple & easy to implement. LR parsing: This is a bottom up parsing alg. that starts with the leaves of the parse tree & constructs the tree by successively reducing terminals. LR par. is more powerful

than LL par. & can handle a larger class of grammars LR(1) par. This is a variant of LR par. that uses lookahead to disambiguate the grammar. LALR par. is a variant of LR par. that uses a reduced set of lookahead symbols to reduce the no. of states in the LR par.

Top down parsing: The proc. of constructing the parse tree which starts from the root & goes down to the leaf is Topdown par. Topdown parsers constructs from the grammar which is free from ambiguity & left recursion. → Topdown parsers use leftmost derivation to construct a parse tree. Bottom up parsing starts from the leaf nodes of a tree & works in upward direction till it reaches the root node. Here, we start from a sentence & then

apply production rules in reverse manner in order to reach the start symbol.

YACC - Yet Another Compiler Compiler: It provides a tool to produce a parser for a given grammar. → YACC is a prog. designed to compile a LALR(1) grammar. → It is used to produce the source code of the syntactic analyzer of the lang. produced by LALR(1) grammar. The file of YACC is the rule of grammar & the file is a C prog. YACC file is divided into 3 parts: 1. definition 2. Rules 3. Aux. routines

the tree by visiting them in some order. In many cases, translation can be done during parsing without building an explicit tree.

Evaluation order for SDD Includes how the SDD (Syn. Directed Definition) is evaluated with the help of att.s, dependency graphs, semantic rules, & S-att. attributed definitions. SDD helps in the semantic analysis in the compiler. It is important to know how SDDs are evaluated & their evaluation order. 1. Dependency graphs: Dep. graph provides info. the order of eval. of att.s with the help of edges.

2. Ordering the eval. of att.s: The dep. graph provides help of edges. An edge from the 1st node att. to the 2nd node att. gives the info. that 1st node att. eval. is required for the eval. of 2nd node att. Ed. represents the semantic rules of the corresponding production. Dep. graph rules → A node in the dep. graph corresponds to the node of the parse tree for each att. Edges of the dep. graph represent that the att. of 1st node is evaluated before the att. of 2nd node. 2. Ordering the eval. of att.s: The dep. graph provides

Syntax Directed Definition (SDD): specifies the value of attributes ~~produced~~ by associating semantic rules with the grammar. It's generalization of context-free grammar in which each grammar production $X \rightarrow a$ is associated with it a set of production rules of the form $S = f(b_1, b_2, b_3, \dots, b_k)$ where S is the att. obtained from func. f . The att. can be str., number, type or a mem. loc. Eg: $E \rightarrow E_1 + T \rightarrow \{E.val = E_1.val + T.val\}$

YACC format: input-file $\rightarrow$ [YACC] $\rightarrow$ y.tab.c y.tab.h $\rightarrow$ [cc] $\rightarrow$ a.out
a input $\rightarrow$ [a.out] $\rightarrow$ o/p [Syntax Directed

Translation: has augmented rules to the grammar that facilitate semantic analysis. SDD involves parsing info. bottom up or topdown to the parse tree in form of att.s attached to the nodes. SDD makes use of lexical values of nodes & constants. att.s associated with non terminals in their definitions. The general approach of SDD is to construct a parse tree or syntax tree & compute the values of att.s at the nodes of

the eval. order att.s of the nodes of the parse tree. An edge in the dep. graph represents that the att. of the 2nd node is dependent on the att. of the 1st node for further eval. This order of evaluation gives a linear order called topological order. We can't evaluate SDD on a parse tree when there is a cycle present in the graph & due to the cycle, no topological order exists. S-att. defined S-att. SDD can have only synthesized att.s. In this type of definition, semantic rules are placed at the end of the production only. Its eval. is based on bottom up

par. Eg: $S \rightarrow ABSS.x = f(A.x, B.x)$. L-att. ributed Definitions: can have both synthesized & inherited. In this type of definition, semantic rules can be placed anywhere in the RHS of the production. Its eval. is based on inorder. Eg: $S \rightarrow ABSS.A.x = S.x + 2$ or $S \rightarrow ABSS.B.x = f(A.x, B.x)$. Semantic rules with controlled side effects: Side effects are the prog. fragment contained within semantic rules. These side effects are ~~in the prog. fragment~~ in SDD can be controlled in 2 ways. Permit partial side effects & constraints on eval. order. It have the same trans. as

Activation tree is a tree struct. that represents func. calls made by a prog. during execution. Activation record Proc. during calls & returns are usually managed by a runtime stack called the control stack. Each live activation has an activation record. Info. needed for each execution of a procedure is stored in a record called activation record. (Return values) Actual param. (ctrl line) (Access link) (saved machine link) (local data) (frame pointer) Storage organization compiler demands a block of mem. from OS to

run compiled prog. This block is called as runtime storage. Runtime storage comes in blocks of contiguous bytes. It is divided into parts to hold code & data. Case 1 static data (stack) → free mem. Heap → Mem. loc. for code are determined at compile time → Locs of static data can also be determined at compile time → Stack - Data obj's allocated at run time → Heap - other dynamically allocated data obj's at run time (for eg. Malloc area in C)

Parameter passing refers to the exchange of info b/w methods, func's & procedures. Some mechanism transfers the val. from a variable procedure to the called procedure. Parameters are 2 types: 1. Actual params are variables that accept data given by the calling proc. These variables are listed in the calling func's definition. They accept calling process's data. 2. Formal param. are variables whose values & func's are given to the called func. These variables are

supplied as param.s in the func. call. R value refers to value contained in the mem. or storage. They are basically placed on right side of the assignment operator. Eg. a = 1; b = a + 7 → R values are 1, a + 7. Location of the expr. refers to the loc. in mem. or storage where the expr. is kept. Eg. x = 10; a = 20 → L values = x, a; R = 10, 20. Methods: 1. call by value: When passing values are passed to formal's. When passing par. the compiler adds the r value of the actual param.s that were passed to

Storage allocation begins in stack storage alloc. If the size of data obj's is known at compile time, names are bound to storage at compile time only. Hence once time procedure is invoked, its names are bound to the same storage alloc. only. Mem. allocated for obj's won't be changed in runtime, hence this alloc. is called static. In this, it is easy for the compiler to find the addresses of the obj's in activation record. Stack alloc. Stack is used for sto. alloc. This stack is called as

Control stack. Almost all lang's have procedures, func's or methods as units of user defined actions. Each time a func. is called, space for its local variables is pushed onto a stack, & when the procedure terminates, that space is popped out of the stack. Heap alloc. If non local variables are to be retained even after the activ. record, then heap alloc. is req'd which is not possible in stack alloc. Heap all. allocations contain blocks of mem. when req'd for storage of activ. records or for other data obj's. It can be deallocated when activ. ends, & it can be reused by heap manager.

the calling procedure, to the called procedure's activation record. Eg. code 2. Call by L-values are passed to formal's. Eg. code swap(*p, *q) & int t, ... 3. Call by copy. L-values are passed to formal's before the call. L-values are determined. 4. Call by name. Actual param.s are substituted for the formal's. Heap management. The heap is the portion of the storage that is used for data that lives indefinitely or until the prog. explicitly deletes it. Garbage Data that can't be referenced is gener-

ally known as garbage. Garbage collection is the proc. of automatically recovering unused computer memory storage. Automatic garb. coll. is used in many high-level lang's.

Code generator converts the internal representation of source code into a form that can be readily executed by the machine. Designing of the code generator should be done in such a way that it can be easily implemented, tested & maintained. → The following are issues arises during the code gen. phase: 1. IP to code generator. 2. Target prog. & mem. management. 3. Instruction selection. 4. Register alloc. issues. Target prog. is the output of the code gen. The o/p may be absolute machine lang., relocatable machine lang. or assembly lang. Abs. machine lang. as o/p has the advantages that it can be placed in a fixed mem. loc. & can be immediately executed.

Basic block is a seq. of consecutive stmts in which flow of ctrl. enters at the begin of the block & leaves at the end without halt or possibility of branching. Flowgraph Once an intern. code prog. is partitioned into basic blocks, we represent the flow of ctrl. b/w them by a graph.

Principal sources of optm. 1. Common Sub expression elimination: An occurrence of an expr. E again is called common sub expr. if E was previously computed, & the values of the variables in E have not changed. we can avoid recomputing the expr. if we can use the previously computed value. Eg | 2. Copy propagation: Assignment of the form $f := g$ called copy stmts. or copy ops. The idea behind copy propagation is to use g for f, whenever possible after the copy stmt. copy propag. means use of one variable instead of another. 3. Dead code elim. 4. Constant folding: The code that can be simplified by the user itself, is simplified. Here, simplification to be done at runtime are with simplified code to avoid additional computations. Peephole optm. A simple but effective tech. for improving the target code is peephole optm., a method for trying to improving the perf. of the target prog. by examining a short

optimization of Basic Blocks. optm. is applied to the basic blocks after the intern. code generation phase of the compiler. Optm. is the proc. of transforming a prog. that improves the code by consuming fewer resources & delivering high speed. In optm., high-level codes are replaced by their equivalent low-level codes. There are 2 types of basic block optm. 1. Struct. preventing transform 2. Algebraic transform 1. → a) dead code elimination b) common subexpression elimination c) naming of temp. variables d) interchange of indep. code

Recent stmts 2 → a) constant folding b) copy propagation c) strength reduction

Dead code elimination: Dead code is defined as the part of the code that never executes during the prog. execution. So, for optm., such code or dead code is eliminated. The code which is never executed during the prog. (dead code) takes time & so, for optm. & speed, it is eliminated from the code. Eliminating the dead code inc. the speed of the prog. as compiler doesn't have to translate the dead code.

seq. of target instruction (called peephole) & replacing these instructions by a shorter or faster seq. whenever possible. Objectives of peephole optm. 1. To improve perf. 2. To reduce mem. footprint 3. To reduce code size. Local optm. Transform. are applied to small blocks of statements. Global optm. Transform. are applied to large prog. segments that includes func.s, procedures & loops. → copy propag. → code motion → dead code eliminations.

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Symbol Table is an important data structure created & maintained by compiler in order to store info. about the occurrence of various entities such as variable names, func. names, obj's, cls's, interfaces etc. Symbol table is used by both the analysis & the synthesis parts of a compiler. Symbol name, type, attr Implementation If a compiler is to handle a small amount of, then the symbol table can be implemented as an unordered list, which is easy to code, but it is only suitable for small tables only. Symbol table can be implemented in one of the following ways.

1. Linear List 2. Binary Search Trees 3. Hash table. 1. List: We use a single arr. or equiv. Value several arr's to store names & their associated info. New names are added to the list in the order in which they are encountered. The position of end of arr. is marked by the pointer available, pointing to where the next symbol entry will go. In implementation using a linked list, a link field is added to each record. Operations on Symbol Table 1. Insert Op. is more frequently used in the analysis phase. Insert()

3 addr. code is a type of interm. code which is easy to generate & can be easily converted to machine code. It makes use of at most 3 addr's & one operator to represent an expr. & the value computed at each instruction is stored in temporary variable generated by compiler. The compiler decides the order of op. given by 3 addr. code. There are 3 representations of 3 addr. code: 1. Quadruple 2. Triplet 3. Indirect Triplet. Translation of expressions: 1. op's within

expr's 2. Incremental transl. 3. Addressing arr. elements 4. Transl. of arr. Reference 1. op's within expr's Eg: $S \rightarrow id = E \rightarrow S.code = E.code || gen(temp.get(id.lexeme))$ $E \rightarrow E_1 + E_2 \rightarrow E.addr = new Temp() E.code = E_1.code || E_2.code || gen(E.addr = E_1.addr + E_2.addr)$

Type checking is the proc. of verifying & enforcing constraints of types in values. A compiler must check that the source prog. should follow the syntactic & semantic conventions of the source lang. & it should

op. is used to insert the info in the symbol table like the unq. name occurring in the source code. 2. lookup. In the sym. tab, lookup() op. is used to search a name. It is used to determine the existence of symbol in the tab. $\rightarrow$ The declaration of the symb. before it is used. Intermediate code generator receives input from its previous phase, semantic analyzer, in the form of an annotated syntax tree. That syntax tree then can be converted into linear representation of postfix notation. Interm. code tends to

the machine indep. code. i.e. code generator assume to have unlimited no. of mem. space to generate code. Eg: $a = b + c * d$ 3 addr. code $r1 = c * d, r2 = b + r1; a = r2$. Interm. code supports eliminating the regm of a new complete compiler. For every individual machine by upholding the same analysis part for all the compilers.

also check the type rules of the lang. It allows the programmer to limit what types may be used in certain circumstances & assigns types to values. The type checker determines whether these values are used appropriately or not. Backpatching

is the activity of filling up unspecified or missing info. of labels using appropriate semantic actions during the code generation phase. Need for Backpatching: 1. Loop can expr. 2. Flow of control statements.

Runtime env. is a state of the target machine, which may include softw. lib's, env.

variables etc to provide services to the process running in the system.